**MTech Semester II Electronics and Communication Engineering**

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| Day | 900-1000 | 1000-1100 | 1100-1200 | 1200-1300 | 1-2 PM | 1400-1500 | 1500-1600 | 1600-1700 |
| M | EC532, VLSI and Embedded Lab-II (0-0-3-3),Dr. Ashish Ranjan,Mrs Prena Rana**OR**EC534, Advanced Communication Lab(0-0-3-3), Dr A. Dinamani Singh |  | LUNCH | EC 502, Semiconductor IC technology (3-0-0-6), Dr. Tarun | EC506, Advance Microwave Engineering(3-0-0-6), Dr. Kalyan Mondal | EC552, Low Power VLSI(3-0-0-6),Dr. Ashish Ranjan |
| T | EC532, VLSI and Embedded Lab-II (0-0-3-3), Dr. Ashish Ranjan, Mrs Prena Rana**OR** EC534 ,Advanced Communication Lab(0-0-3-3), Dr A. Dinamani Singh |  | EC 502, Semiconductor IC technology (3-0-0-6), Dr. Tarun | EC504, Advanced Digital Communication  (3-0-0-6), Dr A. Dinamani Singh | EC552, Low Power VLSI (3-0-0-6),Dr. Ashish Ranjan  |
| W | EC532, VLSI and Embedded Lab-II (0-0-3-3),Dr. Ashish Ranjan, Mrs Prena Rana**OR**EC534 ,Advanced Communication Lab(0-0-3-3), Dr A. Dinamani Singh |  | EC 502, Semiconductor IC technology (3-0-0-6), Dr. Tarun | EC504, Advanced Digital Communication (3-0-0-6),Dr A. Dinamani Singh | Library |
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